

IEEE IITC 2025

June 2-5, 2025 | Westin Josun Busan, **Busan**, Korea

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Blake Hodges is an engineer at the imec USA Nanoelectronics Design Center in Kissimmee, Florida. At imec, he specializes in the cryogenic test and characterization of superconducting devices. In his time at imec, has contributed to the development, characterization, and optimization of superconducting NbTiN interconnects, the first a-Si/NbTiN Josephson junctions, and the first HZO/NbTiN capacitors. Before joining imec, he completed two bachelor's degrees at Georgia Tech in electrical engineering and physics and worked at the Quantum Systems Division at the Georgia Tech Research Institute. While at imec, he has earned a master's degree in electrical and computer engineering, also from Georgia Tech.