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Jongmin Baek received the B.S. (2000), M.S. (2002), and Ph.D. (2006) in material science & engineering from the Seoul National Univ., Seoul, Korea. He has been working at Semiconductor R&D Center of Samsung Electronics.

His main research interest includes deposition processes by CVD and ALD, interconnect integration. Currently he is a manager of dielectric deposition process in interconnects.