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Dr. Jong Myeong Lee is corporate EVP and leader of process development of semiconductor R&D center at Samsung electronics.

He received the B.S., M.S. and Ph.D in inorganic materials science and engineering from Seoul national university.

He joined Samsung electronics in 1998 and has been working on development of various unit processes for DRAM, NAND and logic devices.

He has authored or co-authored more than 100 journal/conference papers and patents.

