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Dr. Kimin Jun is a Master in Samsung Electronics, focusing on logic 3D integration and process development. Prior to Samsung, he worked at Intel Corporation as a research engineer. He has long experiences in exploratory 3D process and architecture R&D, including stacked transistors, BSPDN, hybrid bonding and advanced packaging, in monolithic and heterogeneous manners. Kimin received B.S. and M.S. from Seoul National University, and Ph.D. from Massachusetts Institute of Technology, all majoring in mechanical engineering.