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Dr. Shu-Jen Han

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Dr. Shu-Jen Han joined SeeQC (seeqc.com) in 2021, started as a VP of Engineering and is currently CTO, where he is leading global research and development effort on developing the world's first chip-based digital quantum computing system. He is responsible for building and leading SeeQC's multi-disciplinary teams, constructing long-term/short-term technology roadmaps, and driving the execution of company-level milestones. Han has extensive experience in advancing complex chip technology from basic research to product qualification. He obtained his Ph.D. in Materials Sci. & Eng. and Ph.D. minor in Electric Eng. from Stanford University in 2007. He started his career at IBM semiconductor research and development center working on 45nm and 32nm SOI CMOS technology development (both used in IBM P- and Z- servers), later he managed the nanoelectronics group at IBM's T. J. Watson Research Center working on world-leading beyond-silicon transistor research. In 2017, he joined HFC Semiconductor Corp. as an R&D director, where he led and drove two generations of Magnetic Random Access Memory (MRAM) product development for high-speed, stand-alone applications (256Mb on 55nm and 1Gb on 22nm CMOS platforms). He also led the team to develop MRAM as high-retention embedded flash replacement, which led to multiple licensing deals. Before he left the company, he was an associate VP. His works have been widely reported in media such as CNET, BBC, MIT Technology Review, Fortune, New York Times, and Wall Street Journal etc. He has authored over 100 technical publications with over 15,000 citations, including multiple publications in *Science* and *Nature* series, two book chapters, and over 200 issued US patents.