

# IEEE IITC 2025

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## **Prof. Tetsu Tanaka**

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Tetsu Tanaka received the B.S. and M.S. degrees in electronics engineering and the Ph.D. in machine intelligence and systems engineering from Tohoku University, Japan, in 1987, 1990, and 2003, respectively. In 1990, he joined Fujitsu Laboratories, Ltd., where he researched and developed highly-scaled MOS devices, including DRAM and SOI. From 1994 to 1995, he was a Visiting Fellow at the University of California, Berkeley. In 2005, he moved to Tohoku University and became a Professor at the Graduate School of Biomedical Engineering, Tohoku University 2008. He has been a vice dean of the Graduate School of Biomedical Engineering since 2022. After joining Tohoku University, he developed integrated biomedical micro/nano devices and systems using semiconductor process/device/circuit technologies and neural engineering. His current research topics include 3DIC/TSV technology and analog/digital IC design, a fully implantable retinal prosthesis with a 3D-stacked artificial retina chip, and an intelligent neural probe with Si/flexible materials.